AMENDMENTS TO THE SPECIFICATION

Please amend the first sentence of paragraph 5, page 3, of the Specification as follows:

As illustrated in Figure 1, a Bluetooth integrated circuit ("IC") 106 is typically configured to implement the lower layers of the Bluetooth protocol stack (e.g., the RF ~~130~~ 160, BB ~~140~~ 150, LC ~~150~~ 140, and LM ~~160~~ 130 layers).

The "baseband layer" of paragraph 29, page 12, of the Specification was already designated as "950" so that no amendments are made herein.

Please amend paragraph 34, page 14, of the Specification as follows:

As illustrated in Figure 7a, the host processor may offload operations to the scheduler unit 220 by specifying parameters including a start count value 710, a period 730, a repetition number 740, and one or more actions to be performed 720, 721. The start count 710 specifies a point in time when the scheduler unit 220 should begin executing a specified series of operations. The period 730 indicates the amount of time which should be allocated to perform one iteration of a particular set of actions 720, 721. For example, as illustrated in Figure ~~2b~~ 7b, when implemented in a Bluetooth environment, the period 730 may be defined by a specified number of 625us time slots (e.g., slots 1a-10a; 1b-10b; etc), with each slot representing a different action (e.g., transmit in slot 1a, receive in slot 2a), and a specified hop frequency.

Please amend paragraph 38, page 16, of the Specification as follows:

A trigger signal 662 having a period based on the slot clock 672 period is transmitted as an input to a slot counter 630 which identifies the beginning and end of each time slot. In a Bluetooth environment, the slot counter 630 triggers on a 1/2 slot clock 674 because this is the resolution required by the Bluetooth protocol. In response to the trigger signal 662

and the 1/2 slot clock signal ~~630~~ **674**, a binary control signal 664 is transmitted to a lookup table unit 640.

Please add the following new sentence as the last sentence of paragraph 24, page 10 of the Specification:

Figure 10 illustrates a packet processor 260 according to one embodiment of the invention.